

Abstracts

Application of the PML Absorbing Boundary Condition to the FDTD Analysis of Microwave Circuits

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The perfectly matched layer (PML) absorbing boundary condition is extended for the FDTD calculation of microwave circuits including dielectric boundaries. In addition a new source formulation is proposed, which becomes possible by the use of the PML. The theory is validated by the comparison of measured and calculated results for several microstrip components. It is shown, that the PML can be placed in the extreme nearfield of the structures under investigation without loss of accuracy.

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